



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Diodato et al.
Docket No.: 9-7-17-2
Serial No.: 09/865,847
Filed: May 25, 2001
10 Group: 2115
Examiner: Chun Cao

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *John Mauro* Date: September 14, 2005

Title: Apparatus for Reducing Leakage Power in a Cache Memory by Using Timer Control
Signal Removes Power to Associated Cache Lines

AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. §1.312

20 Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

25 Sir:

A Notice of Allowance and Issue Fee Due, mailed July 11, 2005 has been received in
connection with the above-identified patent application. Please amend the above-identified allowed
30 patent application as indicated on the attached sheets.

*ok to enter
9/23/05
u*